

# LARRY V. WALKER

5214 Mt. Alifan Dr. • San Diego, CA • 92111  
(w): (619) 524-7832 • (c): (480) 215-3041 • E-mail: larry.walker@cox.net

**SUMMARY:** Particularly interested in Test and Evaluation, Communications Systems, Power Systems, and / or Field Engineering positions. I am a technically competent Sr. Electrical Engineer who can manage resources to achieve results: 24+years of solid technical experience and 14+ years in challenging leadership roles.

## **EDUCATION**

**Master of Science Electrical Engineering (MSEE)** – Georgia Institute of Technology, Atlanta, GA, 1987

- Areas: Analog and Power Electronics, Power Systems, Minor: Mathematics

**Bachelor of Science Electrical Engineering (BSEE)** – Georgia Institute of Technology, Atlanta, GA, 1986

- Areas: Analog and Power Electronics, Power Systems

## **PROFESSIONAL EXPERIENCE**

**Naval Officer – United States Navy, NAVSEA and SPAWAR System Commands (Feb. 1993 – present) –**

**Rank: Commander (CDR) / O5, Designator: 1445 (Reserve and Active Duty positions):**

Project Leader, Technical Liaison, and Officer-in-Charge. Responsibilities include managing programs and teams and providing technical support for shipyard, C4ISR, and weapons systems programs. Recalled to active duty. Acquisition Program Manager (APM) for the Sea Navigation Warfare advanced anti-jam GPS antenna.

Previously APM for Digital Modular Radio (AN/USC-61) program, a software-defined, multi-channel radio.

**Electronics Engineer – US Army Electronic Proving Ground, Fort Huachuca, AZ (Sep. 2008 – Sep. 2009):**

Responsible for all aspects of major C4I test and evaluation projects. Test Officer on Joint Tactical Radio System (JTRS) Ground Mobile Radio (GMR) program. Tasks include designing, planning, coordinating, conducting, and reporting tests of advanced electronic systems developed for military use. Includes logistics, environmental, manpower, and admin areas of assigned projects as well as project technical domains.

**Evaluation Lab Manager / Engineer – ON Semiconductor, Phoenix, AZ (Oct. 2000 – Sep. 2008):**

Completely responsible for technical leadership and management of a semiconductor electrical evaluation lab for power discrete, integrated power, small signal (RF) discrete, and standard logic devices. Personally responsible for \$2.5+ million annual budget, 15 personnel, 8,500 sq. ft. of facilities, \$20 million in equipment.

**Electrical Engineer – Motorola, Scottsdale and Phoenix, AZ (Jan. 1988 – Oct. 2000):**

Designed, built, and evaluated analog, RF / microwave, power, and digital electronic circuits and systems. Performed research, analysis, and evaluation of high-energy radiation effects on electronic (mainly communications) systems, subsystems, and components. Designed, built, produced, and evaluated power semiconductors (rectifiers, MOSFETs, thyristors). Reviewed customer requirements, found potential applications for products, and directed new product introductions.

## **ENGINEERING SKILL AND ABILITIES**

**Electrical Test and Evaluation Engineering:**

- Competent in designing, building, and programming computer controlled (GPIB, VXI, RS232) test systems.
- Knowledgeable about wide variety of electronics testing and bench / rack test equipment. Testing experience includes Analog / RF, Power, Digital (high-speed), ESD, EMC, Radiation (ionizing) Effects, and Thermal.

**Software Development and Application:**

- Competent with LabVIEW, HPVEE, VBA, HTML, MS Office (Word, Excel, PowerPoint, Access), SPICE.
- Experience with OrCAD, various Test Executives, C / C++, Visual Basic, Rocky Mountain Basic, UNIX / Linux, FORTRAN, Assembly, and computer networking.

**Electronics Design:**

- Competent at Analog / RF (dc to SHF) / Power, Digital electronics circuit design, simulation, build, & evaluate.

**Nuclear and Space Ionizing Radiation Effects (5+ years experience):**

- Skilled in radiation testing (Total-Ionizing-Dose, Dose-Rate, Single-Event-Effects), radiation environment modeling, shielding analysis, transport calculations, system / circuit effects analysis (TREE), EMP evaluation.

## **QUALIFICATIONS, LICENSES, PUBLICATIONS, AWARDS, PROFESSIONAL ACTIVITIES**

- Defense Acquisition Workforce Improvement Act (DAWIA) Level III – Program Management
- DAWIA Level III – Test and Evaluation; DAWIA Level I – Systems Engineering
- Active DoD Top Secret Clearance (granted Feb. 24, 2009)
- FCC Licensed General Radiotelephone Operator (GROL – Feb. 24, 1998)
- Fully qualified Navy Reserve Engineering Duty Officer (Sept. 9, 1997)
- Numerous Lean Six Sigma courses (no certifications)
- Proficient writing skills - 14 technical publications, 13 engineering bulletins, countless technical reports
- Member, Association of the United States Navy
- 9 professional awards (industry), 5 Navy awards, 3 college awards